In our January 25, 2001 teleconference, Examiner Winder objected to two errors in the specification and asked to correct them by Examiner's amendment. The Examiner then allowed me to review the proposed amendments and asked that I confirm them by calling later.

After reviewing the file, additional amendments to the claims are required.

This supplemental amendment should be entered for the following reasons. The April 12, 2000, action was non final. On September 12, 2000, a response was filed answering all of the issues from the Office action. Therefore, the September 12, 2000, stopped the tolling of time to respond to the April 12, 2000, Office action. Because, the case has not been allowed, applicants may file additional papers including amendments.

Claims 1-4 and 6-26 are now in the application. Claim 1 has been amended. Claim 26 has been added.

The corrections to the specification should remove the errors caused by the submission of new drawings on September 12, 2000.

Amended claim 1 is broader but is still believed to patentable in light of the Examiner's remarks in item 22 of the April 12, 2000, Office action.

Claim 26 is identical to claim 1 that was submitted on September 12, 2000. As indicated in item 22 of the April 12, 2000, Office action and the teleconference of January 25, 2001, this claim should be allowable.

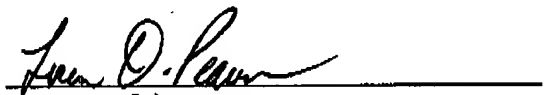
In view of the foregoing, reconsideration and allowance of claims 1-4 and 6-26 are solicited.

In the event the Examiner should still find any of the claims to be unpatentable, please telephone counsel so that patentable language can be substituted.

Please withdraw $98 from the Deposit Account of Lerner and Greenberg, P.A., No. 12-1099 to pay for an additional claim in excess of twenty and an independent claim in excess of three.

Please charge any other fees that might be due with respect to Sections 1.16 and 1.17 to the Deposit Account of Lerner and Greenberg, P.A., No. 12-1099.

Respectfully submitted,



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January 25, 2001

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Amendments to the Specification:

Replace the paragraph beginning at page 11, line 9, of the specification with the following:

-- In the first operating mode according to Fig. 3A, four memory objects S1..S4 are arranged to form a first-in, first out structure (FIFO). Such a FIFO can be designed in a known manner as a ring buffer. In the first operating mode, which is also called the "FIFO operating mode" in the following text, the data objects D1..D3 to be transmitted are written via a write pointer WP, which is connected to the data transmitter, to the data regions B provided for this purpose in the memory objects S1..S4 in the FIFO. In this case, the first data object D1 is written to the data region B of the first memory object S1 in the FIFO. The respectively following data objects D2, D3 are stored in the data regions B of the correspondingly successive memory objects S2, S3.--

Replace the paragraph beginning at page 23, line 17, of the specification with the following:

-- A bus system 10 is designed, for example, to correspond to one of the bus systems 3, 4 shown in Fig. [3] 4. The bus system 10, which in this case is a CAN network, has a multiplicity of network nodes 12, 12' connected to a data bus

11. The memory device 1, which is integrated in Fig. 5 in one of the network nodes 12', is in this case arranged between a first subscriber 3, which is a CAN module, and a second subscriber 4, which is a CPU module. The memory device 1 is in this case used for decoupling the data transfer between the CPU module 3 (operated at high data transmission rates), and the CAN module (operated at relatively low data transmission rates).--

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Amendments to the Claims:

Claim 1 (twice amended). In a data transmission system with at least two subscribers, a memory device to be connected, for serial data transfer of binary data objects of a predetermined data width, between the at least two subscribers, comprising:

a multiplicity of memory objects each being identifiable by a respective address;

each said memory object having a data width being at least as large as a predetermined data width of a data object intended for data transfer, [an identification region containing the respective address of said memory object,] a data region storing the data objects, and a control region containing monitoring and control functions for the data transfer;

at least one FIFO structure containing a plurality of said memory objects and transmitting data in a data-controlled data transfer controlled by the data objects being transmitted.

Please Add the Following Claim:

--26. In a data transmission system with at least two subscribers, a memory device to be connected, for serial data

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transfer of binary data objects of a predetermined data width, between the at least two subscribers, comprising:

a multiplicity of memory objects each being identifiable by a respective address;

each said memory object having a data width being at least as large as a predetermined data width of a data object intended for data transfer, an identification region containing the respective address of said memory object, a data region storing the data objects, and a control region containing monitoring and control functions for the data transfer;

at least one FIFO structure containing a plurality of said memory objects and transmitting data in a data-controlled data transfer controlled by the data objects being transmitted.--